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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/754,727	01/12/2004	Chao-Hsin Lu	LUCH3012/EM	1361
23364	7590 10/04/2004		EXAMINER	
BACON & THOMAS, PLLC			WILLIAMS, HOWARD L	
	625 SLATERS LANE FOURTH FLOOR		ART UNIT	PAPER NUMBER
ALEXAND	RIA, VA 22314		2819	
			DATE MAILED: 10/04/2004	4

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)			
Office Action Summary		10/754,727	LU, CHAO-HSIN			
		Examiner	Art Unit			
		Howard L. Williams	2819			
The MAILING DAT Period for Reply	E of this communication app	ears on the cover sheet with the c	orrespondence address			
THE MAILING DATE OF  - Extensions of time may be availar after SIX (6) MONTHS from the  - If the period for reply specified a  - If NO period for reply is specified.  - Failure to reply within the set or	THIS COMMUNICATION.  able under the provisions of 37 CFR 1.13 mailing date of this communication.   bove is less than thirty (30) days, a reply  l above, the maximum statutory period we  extended period for reply will, by statute,  later than three months after the mailing	IS SET TO EXPIRE 3 MONTH( 6(a). In no event, however, may a reply be time within the statutory minimum of thirty (30) days ill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE date of this communication, even if timely filed	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).			
Status	`					
1) Responsive to con	Responsive to communication(s) filed on					
2a) This action is FINA	2a) This action is <b>FINAL</b> . 2b) This action is non-final.					
/	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims						
4a) Of the above cl 5) ☐ Claim(s) is/a 6) ☑ Claim(s) <u>1-14</u> is/ar 7) ☐ Claim(s) is/a	e rejected.					
Application Papers			•			
10)⊠ The drawing(s) filed Applicant may not re Replacement drawin	quest that any objection to the ogsettion to the ogsettion should be sheet (s) including the correction	r. a)⊠ accepted or b)⊡ objected drawing(s) be held in abeyance. See ion is required if the drawing(s) is obj aminer. Note the attached Office	e 37 CFR 1.85(a). jected to. See 37 CFR 1.121(d).			
Priority under 35 U.S.C. § 1	19					
12) Acknowledgment is a) All b) Some 1. Certified cop 2. Certified cop 3. Copies of the application f	made of a claim for foreign  * c) None of:  lies of the priority documents  e certified copies of the prior  from the International Bureau	s have been received in Applicati ity documents have been receive	on No ed in this National Stage			
Attachment(s)						
1) Notice of References Cited (F		4) Interview Summary				
	ent Drawing Review (PTO-948) ment(s) (PTO-1449 or PTO/SB/08)	Paper No(s)/Mail Da 5)  Notice of Informal P 6) Other:	ate · Patent Application (PTO-152)			

Application/Control Number: 10/754,727

Your Reference: LUCH3012/EM

Art Unit: 2819

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 10-14 are rejected under 35 U.S.C. 102(b) as anticipated by Soda (US 20020186066 A1). Soda discloses an adjustable resistor made from a plurality of MOS devices in parallel receiving a control signal. See figure 3.

Claims 1-8 are rejected under 35 U.S.C. 102(b) as anticipated by Cyrusian et al. (US 20020175758 A1). Cyrusian discloses an adjustable resistor with the series connection of resistors RO shunted (in parallel with) multiple NMOS transistors that receive a predetermined control signal. See figure 5. In paragraph [0024] Cyrusian discloses that the use of MOS was intended to include pmos, nmos and cmos.

Claims 1, 7 and 9 are rejected under 35 U.S.C. 102(b) as anticipated by Isham (US 5,021,747). Isham discloses resistor 33 shunted by transistors 34 and 35 which receive a control signal based on the amplifier output.

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Kovacs et al (US 5,793,239) see the oval enclosed circuit of R1, MP1 and MP2 on the front page. Hirabayashi (US 20030057449 A1) shows a standard element illustration of a field effect resistor in figure 7B.

Any inquiry concerning this communication should be directed to Howard L. Williams at telephone number 571.272.1815. The Patent and Trademark Office has a new central facsimile number for application specific correspondence intended for entry, it is 703-872-9306.

9/29/04 Voice 571.272.1815 Howard L. Williams Primary Examiner Art Unit 2819